

03B24

Variant: [01] - Sample Production

09/05/2024  
V0

RELEASED

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DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational  
design notes .

DESIGN NOTE:  
Example text for critical  
design notes.

DESIGN NOTE:  
Example text for cautionary  
design notes.

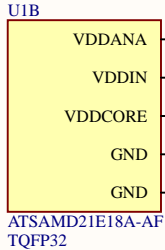
LAYOUT NOTE:  
Example text for critical  
layout guidelines.

	1	2	3	4	5	6	7	8
A	<div>03B24</div> <div>(Block Diagram)</div>							
B								
C								
D								
	1	2	3	4	5	6	7	8

Title <b>03B24</b>		Kronotech Srl Via Adriatica, 284 33030 - Basaldella (UD) Italy
Size: A3	Revision: <b>V0</b>	
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# 03B24

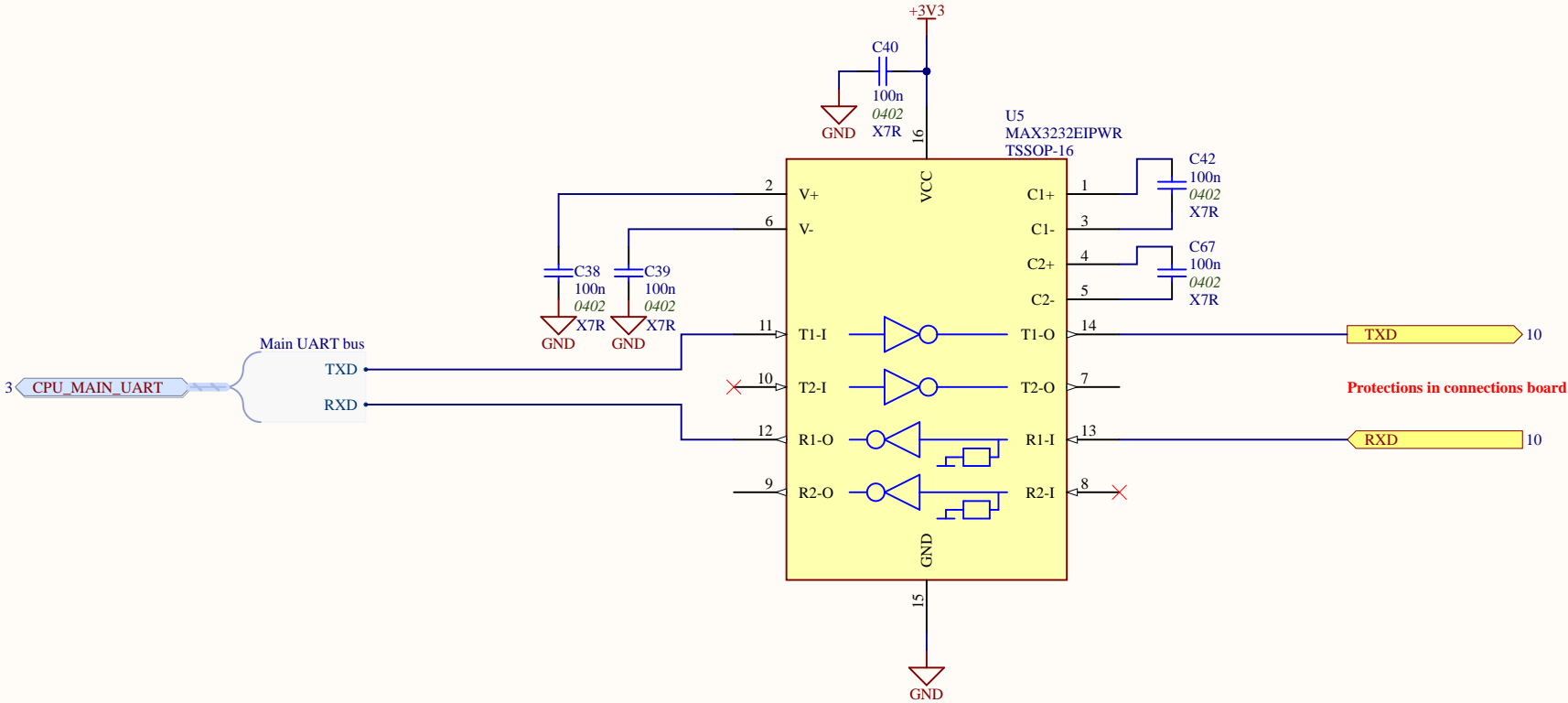
(MCU)



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# 03B24

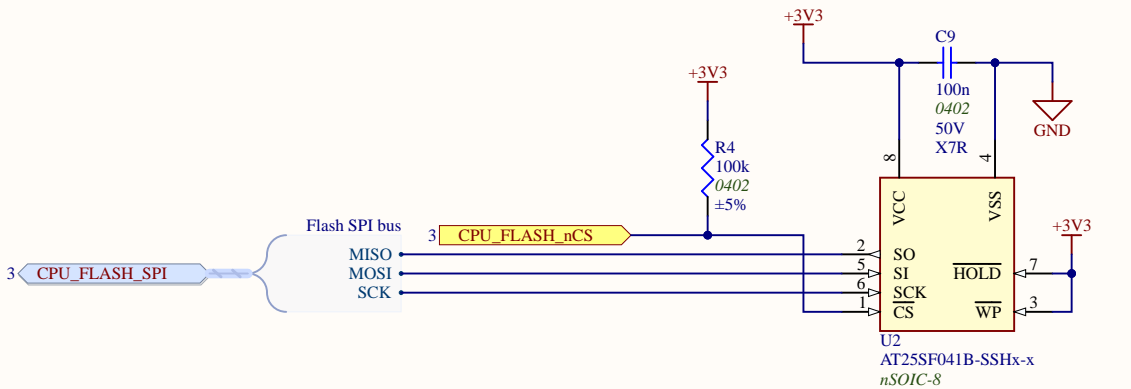
(RS232)



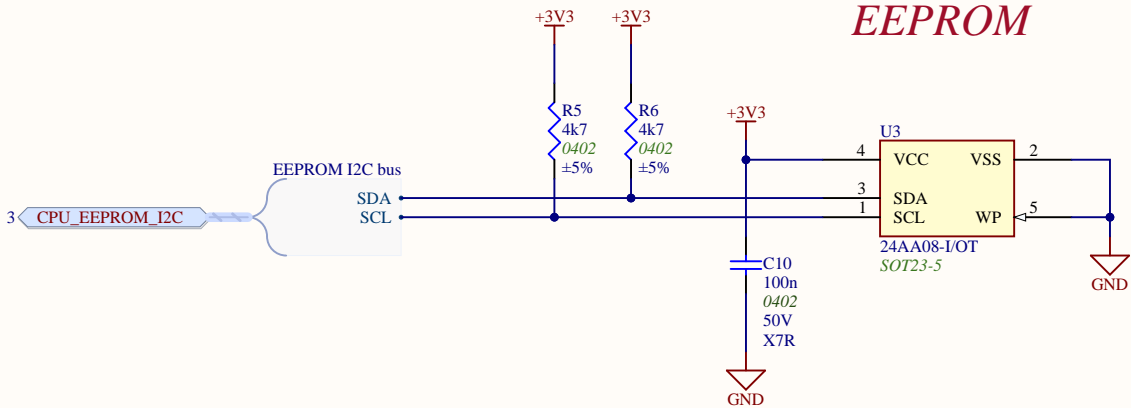
# 03B24

## (Memories)

### FLASH MEMORY

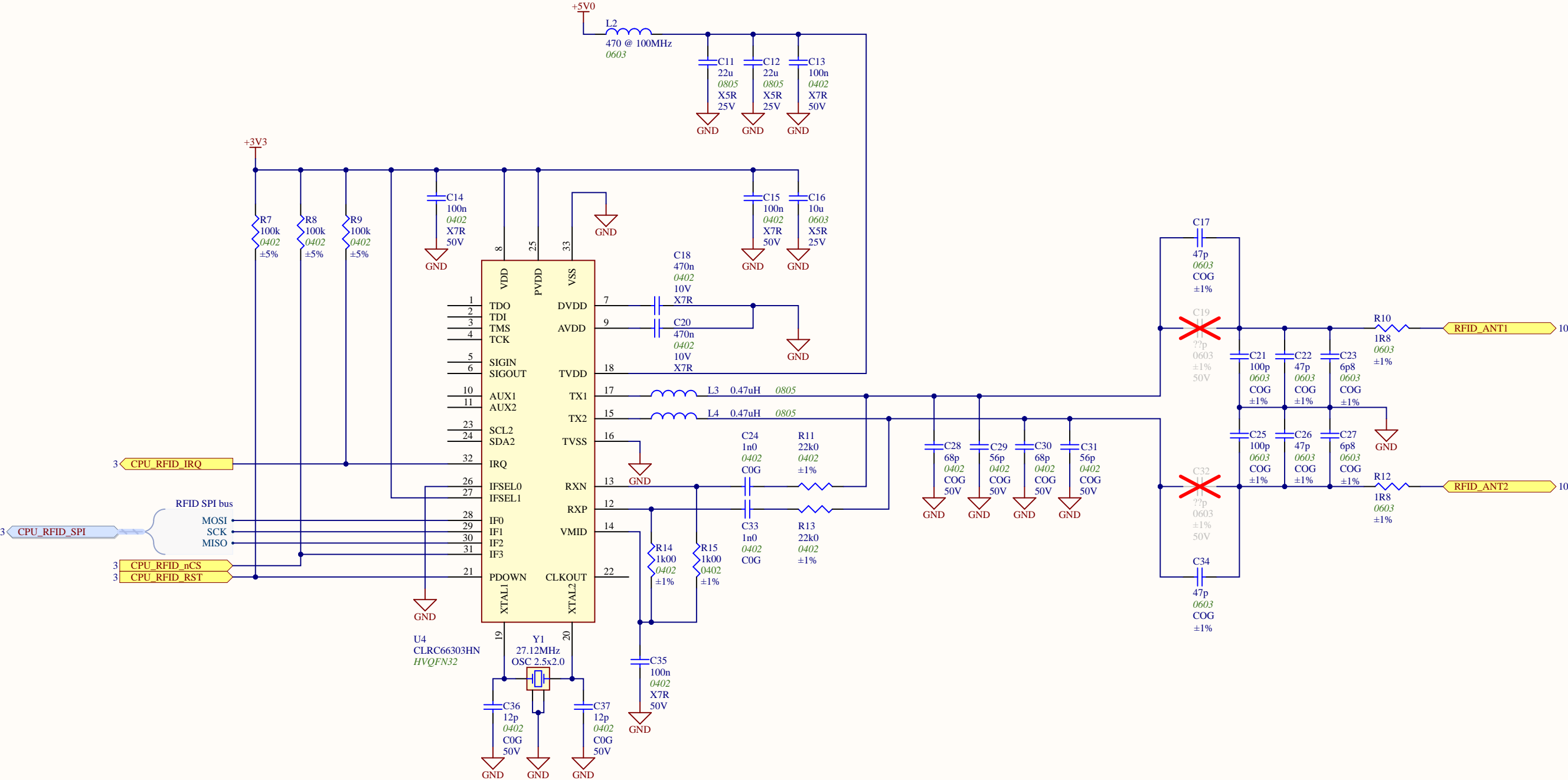


### EEPROM



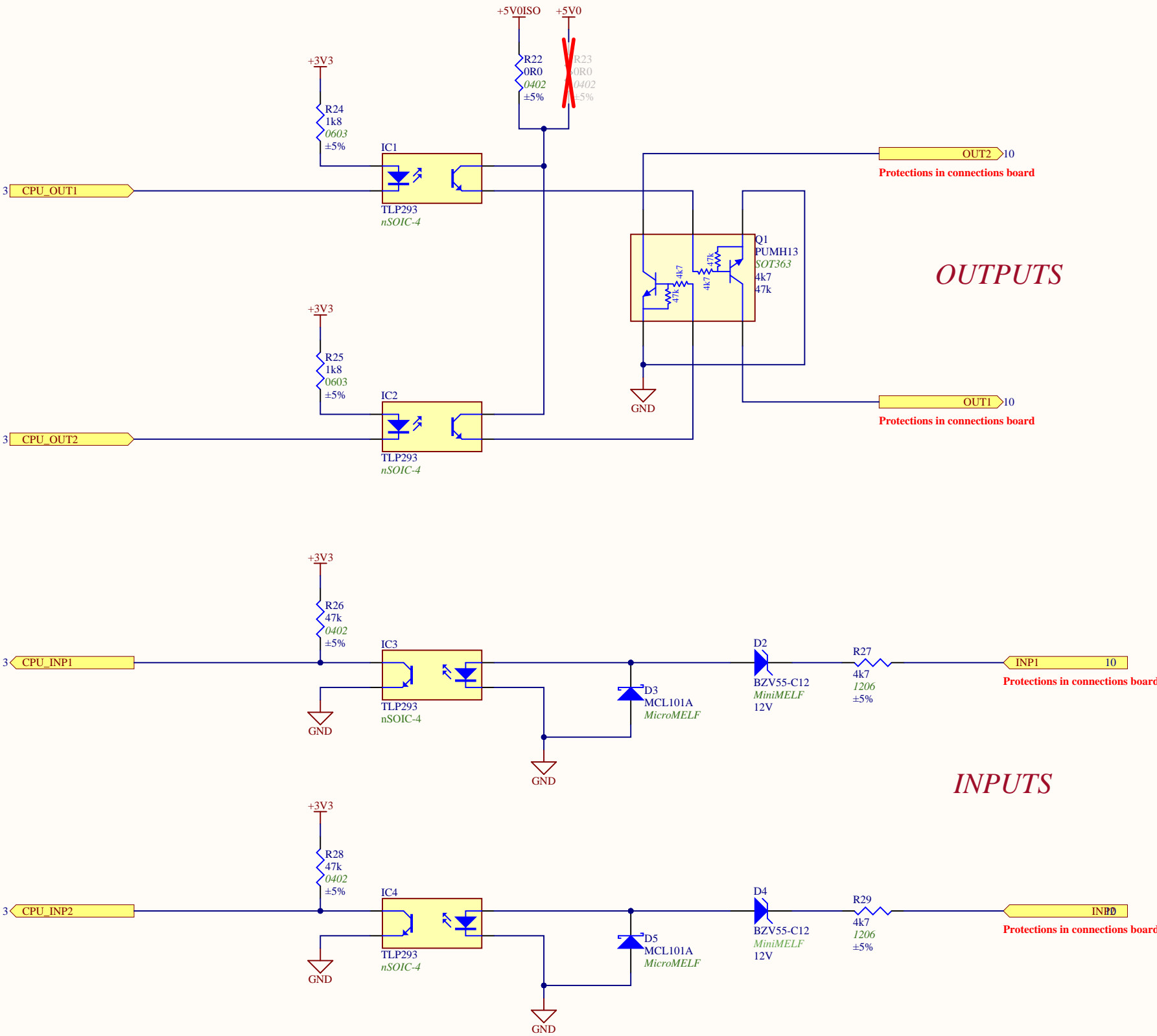
# 03B24

(RFID)



# 03B24

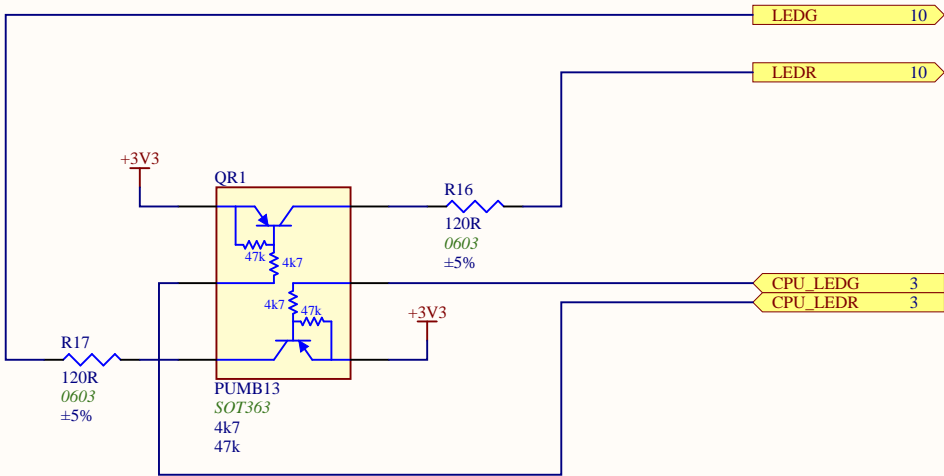
(IO)



# 03B24

(Signaling)

## LEDS

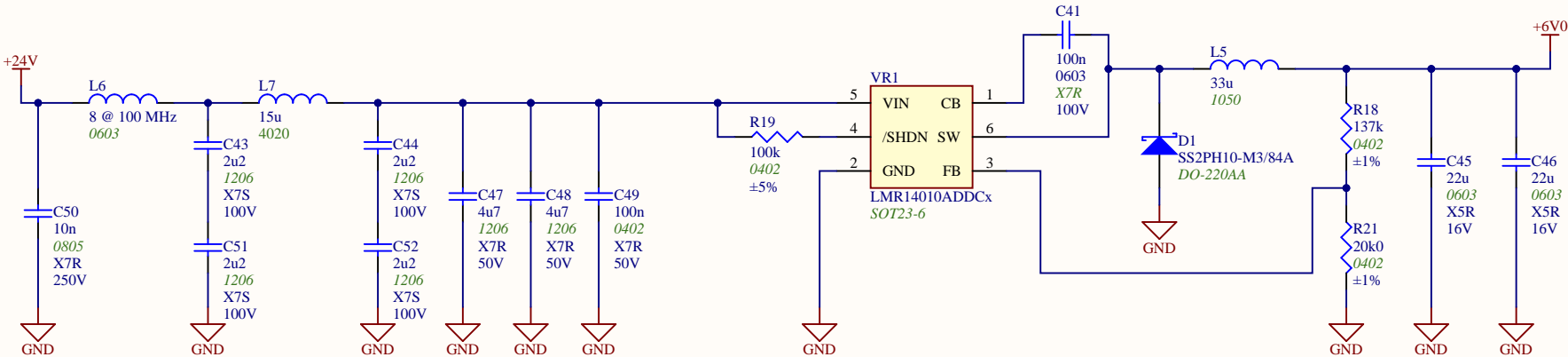




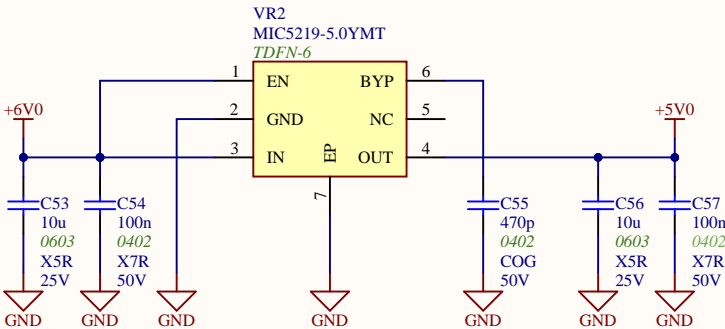
# 03B24

(Power)

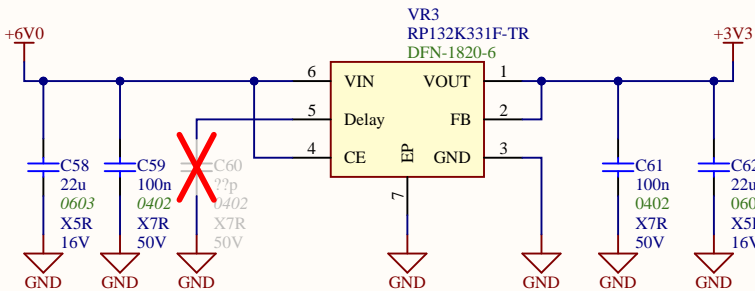
## 6V0



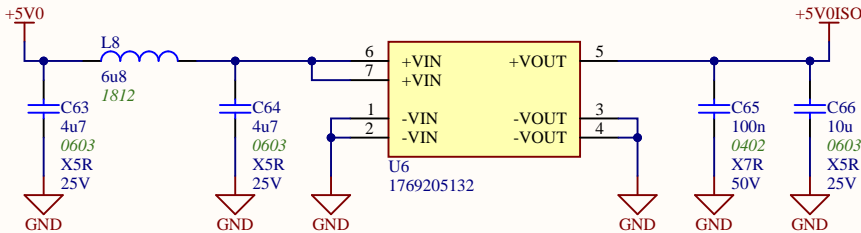
## 5V0



## 3V3

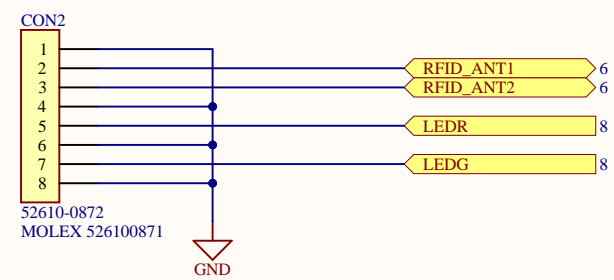
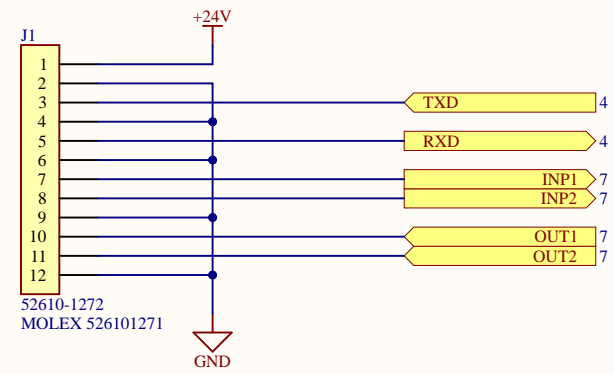


## 5VISO

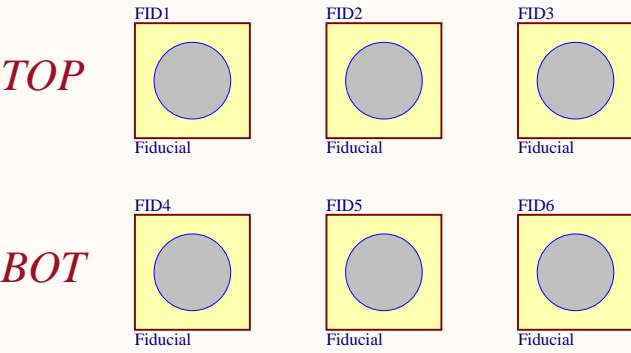


# 03B24

(Connectors)



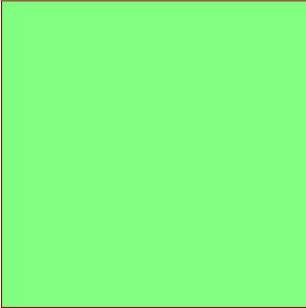
## FIDUCIALS



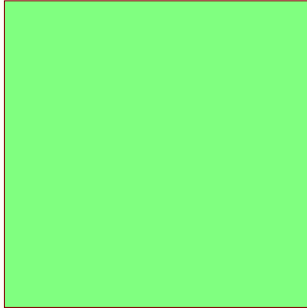
	1	2	3	4	5	6	7	8
A	<div>03B24</div> <div>(Doc Revision History)</div> <div>15/03/2024 - V0 - Initial release</div>							
B								
C								
D	<div>CLOCKS (CPU &amp; PCIe)</div>							
	1	2	3	4	5	6	7	8

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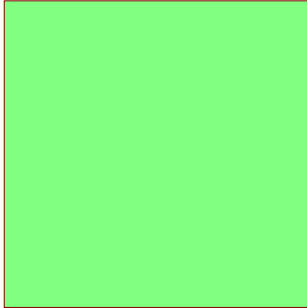
Designator  
[01] - COVER PAGE.SchDoc



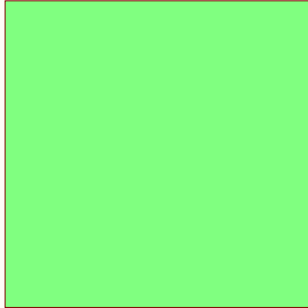
Designator  
[02] - BLOCK DIAGRAM.SchDoc



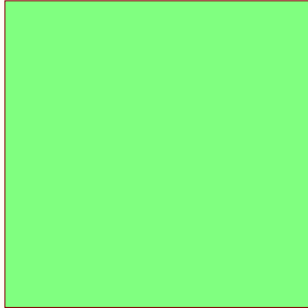
Designator  
[03] - MCU.SchDoc



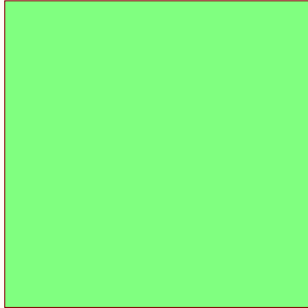
Designator  
[04] - RS232.SchDoc



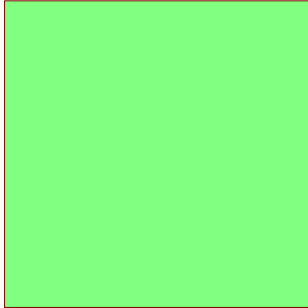
Designator  
[05] - MEMORIES.SchDoc



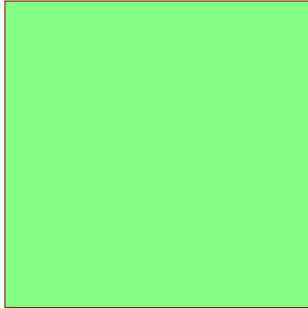
Designator  
[06] - RFID.SchDoc



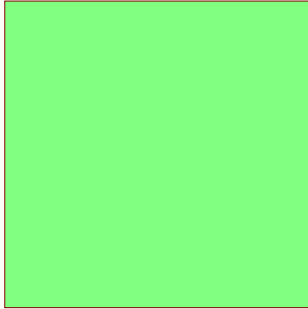
Designator  
[07] - IO.SchDoc



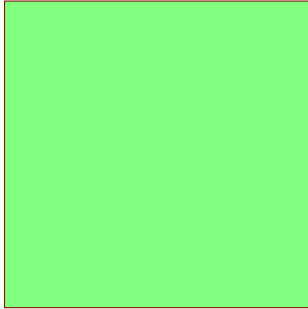
Designator  
[08] - SIGNALING.SchDoc



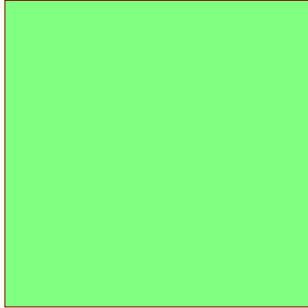
Designator  
[09] - POWER.SchDoc



Designator  
[10] - CONNECTORS.SchDoc



Designator  
[11] - DOC REVISION HISTORY.SchDoc



# TEMPLATE NOTES

## Set Project Parameters

- 1) Go to Project -> Project Options -> Parameters
- 2) Set Project and Version

## Mark Not Fitted Components as

**NF**

## Net Class Example



## Differential signal example



TITLE Examples (You can change the color to reflect your company color)

# PAGE TITLE

*Peripheral / Group of component title*

*Smaller Ttitle*

## Schematic Status Explanation

**DRAFT** - Very early stage of schematic, ignore details.

**PRELIMINARY** - Close to final schematic.

**CHECKED** - There should not be any mistakes. Tell the engineer if you find one.

**RELEASED** - A board with this schematic has been sent to production.

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